WHAT IS CLAIMED IS:

1.	A package substrate adapted to receive an integrated circuit, the package substrate
	comprising:

- an upper contact layer,
- a transmitter power layer,
- a transmitter ground layer,
- a transmitter routing layer disposed between the transmitter power layer and the transmitter ground layer,
- a receiver power layer,
- a receiver ground layer,
- a receiver routing layer disposed between the receiver power layer and the receiver ground layer,
- a lower contact layer,
- electrically conductive contacts disposed in transceiver core contact patterns on the upper contact layer and adapted to make electrical connections with the integrated circuit, each of the transceiver core contact patterns including,
 - two transmitter signal contacts disposed in a transmitter signal contact differential pair and adapted to conduct transmitter signals,
 - two receiver signal contacts disposed in a receiver signal contact differential pair and adapted to conduct receiver signals.
 - transmitter power contacts disposed in a transmitter power contact group and adapted to provide power for only the transmitter signals conducted by the transmitter signal contact differential pair,
 - receiver power contacts disposed in a receiver power contact group and adapted to provide power for only the receiver signals conducted by the receiver signal contact differential pair, and
 - ground contacts adapted to provide ground for both the transmitter signals and the receiver signals,
- electrically conductive transmitter signal traces disposed in transmitter signal trace differential pairs on the transmitter routing layer,

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	differential pairs electrically connecting the transmitter signal trace
	differential pairs with the transmitter signal contact differential pairs,
	electrically conductive transmitter power planes disposed on the transmitter
35	power layer,
	electrically conductive transmitter power vias electrically connecting the
	transmitter power planes with the transmitter power contact groups,
	electrically conductive transmitter ground vias electrically connecting the
	transmitter ground layer with the ground contacts,
40	where a given one of each of the transmitter power planes is associated with and
	aligned with a given one of each of the transmitter signal trace differential
	pairs, where the association is based on the given one of the transmitter
	power planes and the given one of the transmitter signal trace differential
	pairs being electrically connected to contacts disposed within a single one
45	of the transceiver core contact patterns,
	electrically conductive receiver signal traces disposed in receiver signal trace
	differential pairs on the receiver routing layer,
	electrically conductive receiver signal vias disposed in receiver signal via
	differential pairs electrically connecting the receiver signal trace
50	differential pairs with the receiver signal contact differential pairs,
	electrically conductive receiver power planes disposed on the receiver power
	layer,
	electrically conductive receiver power vias electrically connecting the receiver
	power planes with the receiver power contact groups,
55	electrically conductive receiver ground vias electrically connecting the receiver
	ground layer with the ground contacts,
	where a given one of each of the receiver power planes is associated with and
	aligned with a given one of each of the receiver signal trace differential
	pairs, where the association is based on the given one of the receiver

electrically conductive transmitter signal vias disposed in transmitter signal via

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power planes and the given one of the receiver signal trace differential

pairs being electrically connected to contacts disposed within a single one of the transceiver core contact patterns, and

lower electrical contacts disposed on the lower contact layer and electrically connected to the transmitter signal traces, the transmitter power planes, the transmitter ground layer, the receiver signal traces, the receiver power planes, and the receiver ground layer,

where none of the receiver signal traces in the package substrate are disposed on the transmitter routing layer and none of the transmitter signal traces in the package substrate are disposed on the receiver routing layer.

- 2. The package substrate of claim 1, wherein the transmitter routing layer is disposed above the receiver routing layer.
- 3. The package substrate of claim 1, wherein there are no electrically conductive layers disposed between the transmitter routing layer and the transmitter ground layer.
- 4. The package substrate of claim 1, further comprising a second transmitter ground layer disposed below the transmitter power layer.
- 5. The package substrate of claim 1, wherein the transmitter power layer is disposed below the transmitter routing layer and the transmitter ground layer is disposed above the transmitter routing layer.
- 6. The package substrate of claim 1, wherein there are no electrically conductive layers disposed between the receiver routing layer and the receiver ground layer.
- 7. The package substrate of claim 1, further comprising a second receiver ground layer disposed below the receiver power layer.
- 8. The package substrate of claim 1, wherein the receiver power layer is disposed below the receiver routing layer and the receiver ground layer is disposed above the receiver routing layer.

9. A package substrate adapted to receive an integrated circuit, the package substrate comprising: an upper contact layer, a transmitter routing layer, 5 a transmitter ground layer disposed above the transmitter routing layer, a transmitter power layer disposed beneath the transmitter routing layer. a receiver routing layer. a receiver ground layer disposed above the receiver routing layer. a receiver power layer disposed beneath the receiver routing layer, 10 a lower contact layer, electrically conductive contacts disposed in transceiver core contact patterns on the upper contact layer and adapted to make electrical connections with the integrated circuit, each of the transceiver core contact patterns including, two transmitter signal contacts disposed in a transmitter signal contact 15 differential pair and adapted to conduct transmitter signals, two receiver signal contacts disposed in a receiver signal contact differential pair and adapted to conduct receiver signals. transmitter power contacts disposed in a transmitter power contact group 20 and adapted to provide power for only the transmitter signals conducted by the transmitter signal contact differential pair, receiver power contacts disposed in a receiver power contact group and adapted to provide power for only the receiver signals conducted by the receiver signal contact differential pair, and 25 ground contacts adapted to provide ground for both the transmitter signals and the receiver signals, electrically conductive transmitter signal traces disposed in transmitter signal trace differential pairs on the transmitter routing layer, electrically conductive transmitter signal vias disposed in transmitter signal via

differential pairs electrically connecting the transmitter signal trace

differential pairs with the transmitter signal contact differential pairs.

- electrically conductive transmitter power planes disposed on the transmitter power layer,
- electrically conductive transmitter power vias electrically connecting the transmitter power planes with the transmitter power contact groups,

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- electrically conductive transmitter ground vias electrically connecting the transmitter ground layer with the ground contacts,
- where a given one of each of the transmitter power planes is associated with and aligned with a given one of each of the transmitter signal trace differential pairs, where the association is based on the given one of the transmitter power planes and the given one of the transmitter signal trace differential pairs being electrically connected to contacts disposed within a single one of the transceiver core contact patterns,
- electrically conductive receiver signal traces disposed in receiver signal trace differential pairs on the receiver routing layer,
- electrically conductive receiver signal vias disposed in receiver signal via differential pairs electrically connecting the receiver signal trace differential pairs with the receiver signal contact differential pairs,
- electrically conductive receiver power planes disposed on the receiver power layer,
- electrically conductive receiver power vias electrically connecting the receiver power planes with the receiver power contact groups,
- electrically conductive receiver ground vias electrically connecting the receiver ground layer with the ground contacts,
- where a given one of each of the receiver power planes is associated with and aligned with a given one of each of the receiver signal trace differential pairs, where the association is based on the given one of the receiver power planes and the given one of the receiver signal trace differential pairs being electrically connected to contacts disposed within a single one of the transceiver core contact patterns, and
- lower electrical contacts disposed on the lower contact layer and electrically connected to the transmitter signal traces, the transmitter power planes, the

transmitter ground layer, the receiver signal traces, the receiver power planes, and the receiver ground layer,

- where none of the receiver signal traces in the package substrate are disposed on the transmitter routing layer and none of the transmitter signal traces in the package substrate are disposed on the receiver routing layer.
 - 10. The package substrate of claim 9, wherein the transmitter routing layer is disposed above the receiver routing layer.
 - 11. The package substrate of claim 9, further comprising a second transmitter ground layer disposed below the transmitter power layer.
 - 12. The package substrate of claim 9, further comprising a second receiver ground layer disposed below the receiver power layer.
 - 13. A packaged integrated circuit, comprising:

an integrated circuit, and

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a package substrate adapted to receive the integrated circuit, the package substrate including,

an upper contact layer,

a transmitter power layer,

a transmitter ground layer,

a transmitter routing layer disposed between the transmitter power layer and the transmitter ground layer,

a receiver power layer,

a receiver ground layer,

a receiver routing layer disposed between the receiver power layer and the receiver ground layer,

a lower contact layer,

electrically conductive contacts disposed in transceiver core contact patterns on the upper contact layer and adapted to make electrical connections with the integrated circuit, each of the transceiver core contact patterns including,

	two transmitter signal contacts disposed in a transmitter signal
20	contact differential pair and adapted to conduct transmitter
	signals,
	two receiver signal contacts disposed in a receiver signal contact
	differential pair and adapted to conduct receiver signals,
	transmitter power contacts disposed in a transmitter power contact
25	group and adapted to provide power for only the transmitter
	signals conducted by the transmitter signal contact
	differential pair,
	receiver power contacts disposed in a receiver power contact group
	and adapted to provide power for only the receiver signals
30	conducted by the receiver signal contact differential pair,
	and
	ground contacts adapted to provide ground for both the transmitter
	signals and the receiver signals,
	electrically conductive transmitter signal traces disposed in transmitter
35	signal trace differential pairs on the transmitter routing layer,
	electrically conductive transmitter signal vias disposed in transmitter
	signal via differential pairs electrically connecting the transmitter
	signal trace differential pairs with the transmitter signal contact
	differential pairs,
40	electrically conductive transmitter power planes disposed on the
	transmitter power layer,
	electrically conductive transmitter power vias electrically connecting the
	transmitter power planes with the transmitter power contact
	groups,
45	electrically conductive transmitter ground vias electrically connecting the
	transmitter ground layer with the ground contacts,
	where a given one of each of the transmitter power planes is associated
	with and aligned with a given one of each of the transmitter signal
	trace differential pairs, where the association is based on the given

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one of the transmitter power planes and the given one of the transmitter signal trace differential pairs being electrically connected to contacts disposed within a single one of the transceiver core contact patterns,

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electrically conductive receiver signal traces disposed in receiver signal trace differential pairs on the receiver routing layer,

electrically conductive receiver signal vias disposed in receiver signal via differential pairs electrically connecting the receiver signal trace differential pairs with the receiver signal contact differential pairs,

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electrically conductive receiver power planes disposed on the receiver power layer,

electrically conductive receiver power vias electrically connecting the receiver power planes with the receiver power contact groups,

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electrically conductive receiver ground vias electrically connecting the receiver ground layer with the ground contacts,

where a given one of each of the receiver power planes is associated with and aligned with a given one of each of the receiver signal trace differential pairs, where the association is based on the given one of the receiver power planes and the given one of the receiver signal trace differential pairs being electrically connected to contacts disposed within a single one of the transceiver core contact patterns, and

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lower electrical contacts disposed on the lower contact layer and electrically connected to the transmitter signal traces, the transmitter power planes, the transmitter ground layer, the receiver signal traces, the receiver power planes, and the receiver ground layer,

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where none of the receiver signal traces in the package substrate are disposed on the transmitter routing layer and none of the transmitter signal traces in the package substrate are disposed on the receiver routing layer.

- 14. The packaged integrated circuit of claim 13, wherein the transmitter routing layer is disposed above the receiver routing layer.
- 15. The packaged integrated circuit of claim 13, wherein there are no electrically conductive layers disposed between the transmitter routing layer and the transmitter ground layer.
- 16. The packaged integrated circuit of claim 13, further comprising a second transmitter ground layer disposed below the transmitter power layer.
- 17. The packaged integrated circuit of claim 13, wherein transmitter power layer is disposed below the transmitter routing layer and the transmitter ground layer is disposed above the transmitter routing layer.
 - 18. The packaged integrated circuit of claim 13, wherein there are no electrically conductive layers disposed between the receiver routing layer and the receiver ground layer.
 - 19. The packaged integrated circuit of claim 13, further comprising a second receiver ground layer disposed below the receiver power layer.
 - 20. The packaged integrated circuit of claim 13, wherein receiver power layer is disposed below the receiver routing layer and the receiver ground layer is disposed above the receiver routing layer.